Form PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY DOCKET NO. 98-179/1496.00065	SERIAL NO 09/183,694
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U.S. PATENT DOCUMENTS

Examiner Initial	Document No.							Date	Name	Class	Sub-Class	Filing Date
✓	4	6	1	5	0	0	1	9/30/1986	Hudgins, Jr.	364	200	
✓	5	1	4	6	5	7	8	9/8/1992	Zangenehpour	395	425	
✓	5	2	0	8	7	4	5	5/4/1993	Quentin et al.	364	188	
✓	5	8	7	0	5	8	3	2/9/1999	Maeda	395	497.01	
✓	6	1	0	8	7	2	2	8/22/2000	Troeller et al.	710	26	
✓	6	1	4	1	7	3	1	10/31/2000	Beardsley et al.	711	136	
✓	6	1	5	4	7	9	3	11/28/2000	MacKenna et al.	710	23	

FOREIGN PATENT DOCUMENTS

	Document No.							Date	Country	Class	Sub-Class	Transl. Yes	Transl. No

OTHER DOCUMENTS (Incl. Author, Title, Date, Pertinent Pages, etc.)

✓	"High Performance DMA Controller/CPU Interface Mechanism", IBM Technical Disclosure Bulletin, Vol. 36, No. 02, February 1993, pp. 131-133.

(Stamp: RECEIVED MAR 0 8 2001 Technology Center 2100)

Examiner	*(signature)* Elwood Pak	Date Considered	4/24/2001

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